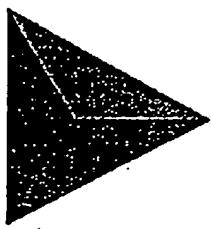


Exhibit B

Efficient Mixing of Sequential Prefetches With Random Access Data in a Preexisting LRU Cache

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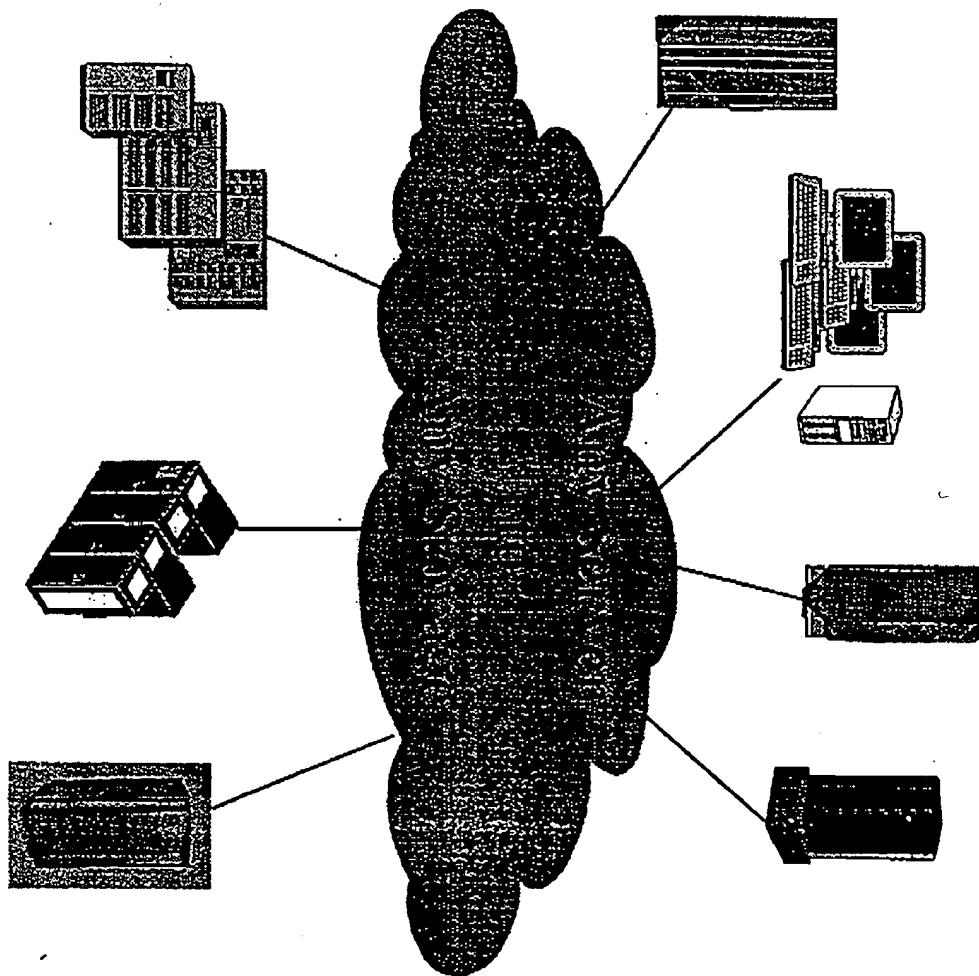


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Efficient Prefetching w/LRU...

- Title:
 - ▶ ■ Efficient Mixing of Sequential Prefetches with Random Access Data in a Preexisting LRU Cache.
- Product Name:
 - none (several candidates: CF, SAN)
- First Date Disclosed:
 - Has not been disclosed.
- Problem Solved:
 - Prefetch optimization in a black-box LRU-caching system.

Example use of the process in a SAN environment



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Problem

- LRU cache logic sometimes inaccessible or undesirable to alter.
- LRU cache often accessed by external hosts or multiple hosts.
- Prefetched data is ideal for sequential accesses, which works poorly with LRU.
- Identifying prefetch candidates difficult.

Solution

- Solution is to:
 - Use model of prefetching effects to make decisions.
 - Estimate single ref. residency time.

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Efficient Prefetching w/LRU

- Benefit over prior art:
- Competitors:
 - ▶ Improved identification of prefetch candidates.
- Will others want to use it?
- Discoverable?
 - ▶ I/O requests in addition to workloads together with a moving prefetch criteria.
 - ▶ how easily?
- Alternatives?
 - Static prefetch determination.
 - Modification of LRU not required.
 - Companies trying to preload selected elements in an LRU cache.
 - Consider a SAN with a cached control unit to preload into.

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The process for efficient prefetching w/LRU

- Determine cache size.
- Periodically fetch Hit Ratio, cached I/O rate and estimate the SRRT.
- For each I/O check the model's buffer, use previous element's sequential count.
- If above a dynamic threshold, prefetch.

The process for efficient prefetching w/LRU (continued)

- Load I/O requests + prefetches into model's buffer. Update LRU position on hit. Discard on overflow.
- Keep track of overflow point for each of multiple prefetch threshold criteria, counting prefetched "hits" for each criteria.
- If alternate prefetch criteria value is better, then consider adjustment.

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Description of Invention

- Inputs:
 - Cache Size
 - periodic:
 - Hit Ratio feedback
 - Cached I/O Rate feedback
- Output:
 - Prefetch candidates identified
 - Optimal prefetch threshold identified
 - Value of prefetching quantifiable

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